## Docket No. AMENDMENT TRANSMITTAL LETTER BOUND 3.0-131 DIV III Filing Date Art Unit Application No. Examiner 10/715,267-Conf. #4590 November 17, 2003 S. MacArthur 1792

Applicant(s): Nathan R. Brown

METHODS FOR POLISHING SEMICONDUCTOR DEVICE STRUCTURES BY Invention: DIFFERENTIALLY APPLYING PRESSURE TO SUBSTRATES THAT CARRY THE SEMICONDUCTOR DEVICE STRUCTURES

## TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

	CLAIMS AS AMENDED							
	Claims Remaining After Amendment	Higher Numbe Previou Paid	er	Number Extra Claims Present		Rate		
otal Claims	28	- 31	=	0	х	52.00	0.00	
ndependent Claims	3	- 3	-	0	х	220.00	0.00	
Multiple Depend	ent Claims (ch	eck if appl	licabl	e)				
Other fee (please	e specify):							
TOTAL ADDITI	ONAL FEE F	OR THIS	AME	NDMENT:			0.00	
x Large Entity						Small Entity		
x No additiona	l fee is require	d for this	amer	ndment.	_			
=					the e	mount of \$		
_								
_					the filir	ng fee is enclosed	1.	
Payment by	credit card. Fo	orm PTO-	2038	is attached.				
	is hereby auth below. A dup					sit Account No ed.	12-1095	
x Credit ar	ny overpaymer	nt.						
x Charge a	ny additional fil	ing or appl	licatio	n processing f	ees red	quired under 37 CF	R 1.16 and 1.1	
John J. Farrell/						Dated: Ju	ly 14, 2010	
John J. Farrell	Rea. No.: 57.:	291						
Attorney/Agent				LZ & MENTL				

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted via the Office electronic filing system in accordance with § 1.6(a)(4).

Dated: July 14, 2010 Electronic Signature for John J. Farrell: /John J. Farrell/